



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2814
Examiner: Anh D. Mai

#11/B
OK
4/3/02

In Re PATENT APPLICATION Of:

Applicant : Hiroki NAKAMURA)
Serial No. : 09/625,178)
Filed : July 25, 2000)
For : SEMICONDUCTOR DEVICE HAVING)
WIRING PATTERNS COVERED WITH)
INSULATING LAYER (AMENDED))
Attorney Ref. : F98ED0762)

AMENDMENT

RECEIVED
APR - 1 2002
TECHNOLOGY CENTER 2800

March 28, 2002

BOX: Fee Amendment
Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Examiner's Office Action mailed December 28, 2001,
please amend the above-identified application as follows:

IN THE DRAWING

Attached is a "Submission of Proposed Red-Marked Drawing with Formal simultaneously". One (1) of two (2) attached sheets is corrected red-marked drawings and the other attached sheet is corrected formal drawings.

THE TITLE OF THE INVENTION

Please amend the title of the invention to read as follows.

SEMICONDUCTOR DEVICE HAVING WIRING PATTERNS COVERED
WITH INSULATING LAYER

IN THE CLAIMS:

Please amend claims 1, 3-5, 10, 11, 13-16, 19 and 20 as follows, and add new claims 27-33.

1 (amended). A semiconductor device, comprising:

a semiconductor substrate having a circuit area where an integrated circuit is formed and a peripheral area surrounding the circuit area;